

Concurrent Systems

Nebenläufige Systeme

VII. Semaphore

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Agenda

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Fundamentals

Classification

Characteristics

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 - with the **general semaphore** as a measure that supports both
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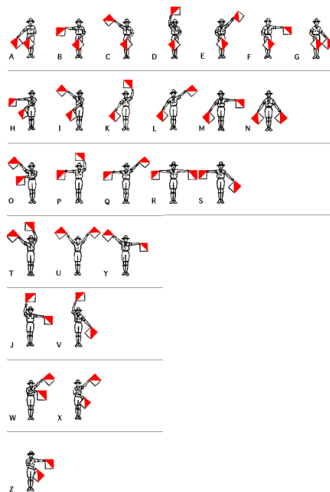
- elaboration of various implementation aspects regarding both types of semaphore as well as mutex as an entity





(Ger.) *Signalmast, Formsignal*



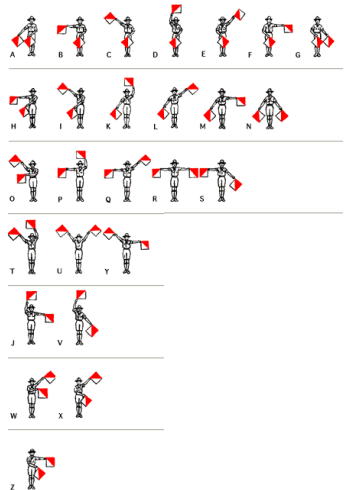


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Concept for Cooperation and Communication

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- also referred to as **counting semaphore** (Ger. *zählender Semaphor*)



Elementary Operations

- insensitive to the distinction between binary and general semaphore is the definition of two **intrinsic primitives** [1]:

P abbr. for (Hol.) *prolaag*; a.k.a. *down*, *wait*, or *acquire*, resp.

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 - i iff the resulting value would be non-negative [2, p. 29]
 - ii non-constraining [3, p. 345]
 - blocks the process iff the value is or was, resp., 0 before decrease
 - blocking processes are put on a **waitlist** associated with each semaphore

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Hint (Waitlist)

*The **queuing discipline** rivals with planning decisions of the process scheduler and, thus, may be the cause of critical **interference**.*

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- **multilateral synchronisation** [5, p. 15] of interacting processes
 - the critical section is considered as a **non-preemptable reusable resource** that needs to be allocated indivisibly to a process to be usable correctly
 - in logical respect, the process having completed P on semaphore S is the only one being authorised to complete V on S

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Hint (Mutex (cf. p. 37/43))

A mutex is a **binary semaphore** that incorporates an **explicit check for authorisation** to release a critical section in the moment of V .



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 - in the end, the data store will have to be deallocated and, thus, made available again by the “data consumer”
- from this it follows that P and V applied to the same semaphore S must have to be accomplishable by different processes, normally
 - which makes the big difference to a binary semaphore or mutex, resp.



```
1 semaphore_t data = {0};
2
3 void producer() {
4     for (;;) {
5         /* data released */
6         V(&data);
7     }
8 }
9
10 void consumer() {
11     for (;;) {
12         P(&data);
13         /* data acquired */
14     }
15 }
```

- default value is 0
 - P must block out only if there is no data
 - V indicates more data
 - calling sequence
 - V must be actable independent of P
 - in order to complete, P depends on V
- beware of an **overflow** of the values margin



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- usually, producer and consumer are different interacting processes
 - in case of one and the same process, the number of a completed V must exceed the number of a completed P in order to prevent deadlock
 - $\#V > \#P$, which implies a path $V \rightarrow P$ (i.e., V “happens before” P)

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1 semaphore_t store = {N};
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4     for (;;) {
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7     }
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10 void consumer() {
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- as to interacting processes in the line of producer and consumer, the same applies as mentioned before: $\#V > \#P$
- in other cases: $\#V \leq \#P$, must be completed by the same process



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A means of managing an unlimited number of consumable resources on the basis of a limited number of reusable resources.



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```

- indisputable classic in cooperation and communication of processes
 - simply a merge of the semaphore use pattern discussed as before
 - **transverse application** of P and V to a pair of general semaphores



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 - by extending a binary semaphore, P will have to record and V will have to check ownership of CS



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 - values $S > 1$ must be prevented either by the use pattern or by the implementation of P and V
- if **authorisation fails**, the process attempting to release CS should be aborted—in kernel mode, the computing system must be halted...



Hint (Computer Science Folklore)

A semaphore can be released by any process.

²see also p. 121



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- incomplete or rough, if not broad-brush, phrase that must be regarded with suspicion—one have to distinguish between semaphore types²
 - strictly, essence of this phrase is **requirement** for a general semaphore
 - strictly as well, it is merely an **option** for a binary semaphore
 - in logical respect, a binary semaphore may not be released by any process
 - in physical respect, this however is not a must for any implementation

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- a phrase that is slanted towards only one aspect as to the leastwise twofold non-uniform common understanding about a mutex:
 - i a category of **methods** for ensuring mutual exclusion *or*
 - ii the **implementation** of one of these methods in terms of an **entity**²

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 - in functional terms, however, P and V need not be system calls
 - in non-functional terms, P and V should be close to the **scheduler**
 - by settling P and V in the address space of the operating-system kernel *or*
 - by making scheduler functions available through “strawweight” system calls

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Waitlist Association

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Hint (Process-Table Walk—Conformance to Scheduling)

Part of the scheduler, lookup function to locate a process descriptor on the basis of the blocked-on mark as search key.



- in the absence of simultaneous processes, the implementation of a semaphore could be as simple as follows:

```
1 void prolaag(semaphore_t *sema) {  
2     if (!claim(sema))    /* at the moment, unavailable */  
3         sleep(&sema->wand);  
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6 void verhoog(semaphore_t *sema) {  
7     if (unban(sema))     /* as from now, available */  
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- whereat *claim* decreases and *unban* increases the value of the semaphore according to binary or general, resp., characteristic⁴

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- whereat *claim* decreases and *unban* increases the value of the semaphore according to binary or general, resp., characteristic⁴
- but, assuming that the presence of simultaneous processes is possible, this implementation shows a **race condition** $\leadsto$ **lost wakeup**
 - 3 ■ while going to sleep, i.e. being “sleepy”, the process gets delayed
 - 7–8 ■ but in good faith of a sleeper, the “sleepy” process may be missed

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- P and V itself constitute a **critical section**, likewise, that must be protected in order to function correctly
 - protection should be constructed **per semaphore instance**, not P/V

```
1 void prolaag(semaphore_t *sema) {
2     atomic *sema = {
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```

Deadlock Prevention

Provided that protection of the critical section on the P side is not deregulated, the V side will never complete and, thus, will never cause unblocking of a process:

- the right location for deregulation is *sleep*
- after the process was marked sleeping

- as a process will have to block inside a critical section, **deregulation of protection** is indispensable for the period the process is blocked



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⁵abbr. for *first- or second-level interrupt handling*, resp.



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 - inhibit FLIH⁵, postpone SLIH⁵, or lock process
 - problem-specific construction of an **enter/leave** pair
 - coming right up next in this lecture (cf. p. 71ff.)

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non-blocking ■ fall back on the elementary operations of the ISA level
■ problem-specific construction of P and V
→ coming up as a case study in the context of LEC 10/11

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 - upon return from *sleep* a formerly blocked process may complete P by mistake, joining a process in the critical section to be protected by P
 - this is because completion of V also opens the door for any process, not only for a process having been blocked at the semaphore
- if applicable, aroused processes have to **retry claiming**: `if` $\mapsto$ `while`

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 - if applicable, aroused processes have to **retry claiming**: **if** $\mapsto$ **while**
- not least, concurrency had to be constricted to no more than what is absolutely necessary: reflect on *claim/sleep* and *unban/rouse*

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Outline

Preface

Fundamentals

Classification

Characteristics

Implementation

Data Structures

Functions

Mutex

Summary



Semaphore Data Type

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2      int gate;                /* value: binary or general */  
3      wand_t wand;            /* protective shield */  
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- a wand that takes care of **mutual exclusion** techniques by means of locks [8], for example, could be the following:

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1 typedef volatile struct wand {  
2     lock_t clue;             /* protects P or V, resp. */  
3     event_t wait;           /* list of sleeping processes */  
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→ becoming acquainted with other wands is content of future lectures...



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2     enter(&sema->wand);          /* avert overlapped P or V */
3     lodge(sema);                  /* raise claim to proceed */
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6     leave(&sema->wand);           /* allow P or V */
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 - 4 ■ if applicable, “when” takes care of overtaking processes
 - 11–12 ■ if applicable, search for sleepers happens unconditionally
 - in case of (i) logical waitlist and (ii) strict binary semaphore



Acquire and Release Semaphore I

- load/store-based implementation for a **binary semaphore**:

```
1  inline int lodge(semaphore_t *sema) {  
2      return 42;  
3  }  
4  
5  inline bool avail(semaphore_t *sema) {  
6      return (sema->gate == 0) ? false : !(sema->gate = 0);  
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- also note the persisting sensitivity to simultaneous processes: *avail*
 - use within a safeguarded program section is assumed...



Acquire and Release Semaphore II

- enumerator-based implementation for a **general semaphore**:

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1  inline int lodge(semaphore_t *sema) {  
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4  
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Hint (erroneous rouse)

Caused by misuse of V or by forced and uncontrolled unblocking of a process that went to sleep in P . Both are programming errors: the former at (semaphore) application level, the latter at system level.




```
1 inline void sleep(wand_t *wand) {
2     catch(&wand->wait); /* disclose process to V */
3     leave(wand);        /* allow P or V */
4     coast();            /* take a break */
5     enter(wand);        /* apply for return to P */
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8 inline void rouse(wand_t *wand) {
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 - 2 ■ endorse interest of the current process of upcoming dormancy
 - 3 ■ soon dormant process was made known, deregulate P safeguard
 - 4 ■ transition to dormant state: rescheduling, context switch or idleness
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- catch* ■ exists in two variants, depending on the waitlist model (cf. p 55):
- i store of a blocked-on mark in the process descriptor *or*
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- variant (i) writes to an own data structure of the current process, while variant (ii) manipulates a shared data structure
- signalises upcoming blocking (dormancy) of the registered process



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- coast* ■ blocks the current process, reschedules the processor, and either performs a context switch or runs through the idle loop
- manipulates a shared data structure (ready list)
 - performs the queuing function of the queue-based *catch*
- eventually returns when the blocking condition was nullified



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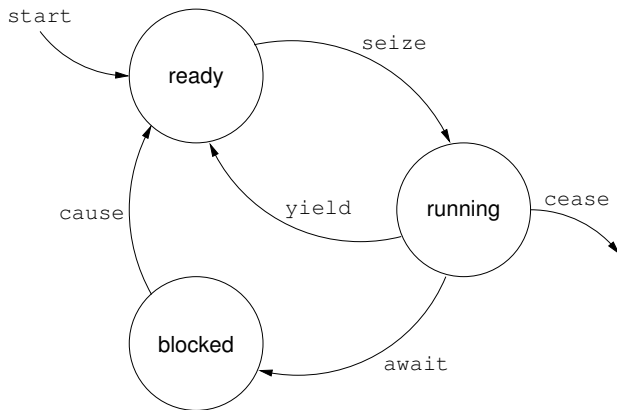


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Hint (Idle State (cf. p. 47 and p. 122))

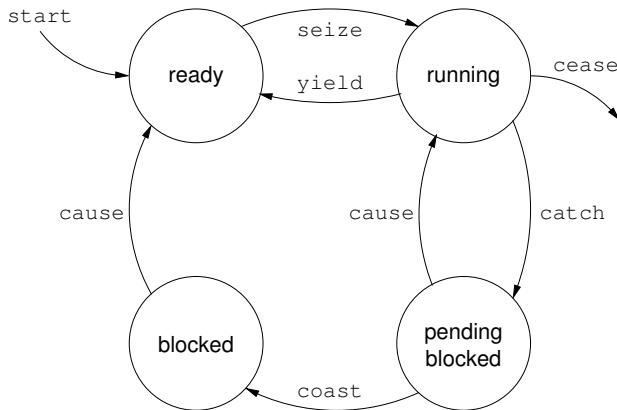
The last process blocked may find itself on the ready list. Same may happen to the “sleepy process” as coast runs deregulated to P/V.





- $\text{ready} \leftrightarrow \text{running}$ ■ scheduler
- $\text{blocked} \rightarrow \text{ready}$ ■ iff *effective signalling* (V), i.e., waiting process
- $\text{running} \rightarrow \text{blocked}$ ■ P , intermediate step needed: prevent *lost wakeup*





- **ready** $\leftrightarrow$ **running** ■ scheduler
- **blocked** $\rightarrow$ **ready** ■ iff *effective signalling* (V), i.e., waiting process
- **running** $\leftrightarrow$ **pending** ■ *doze* ($P \rightarrow$), *effective signalling* ($\leftarrow V$)
- **pending** $\rightarrow$ **blocked** ■ *deep sleep* (P), no overlapping V



- as there is no single solution to protect P and V adequately, the wand attribute symbolises intention to application orientation
 - depending on the mode of operation or use case, the wand acts differently
 - assuming that processing elements are not multiplexed [7, p. 5], then:

```
1 inline void enter(wand_t *wand) {  
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3 }  
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6     unlock(&wand->clue);  
7 }
```

- wand capability depends on the “type of exclusion” in relation to the required characteristics of the operating system machine level:
 - partial** ■ processor **multiplexing** $\leadsto$ interrupt control
 - mutual** ■ processor **multiplication** $\leadsto$ process lock, see example above



- as there is no single solution to protect P and V adequately, the wand attribute symbolises intention to application orientation
 - depending on the mode of operation or use case, the wand acts differently
 - assuming that processing elements are not multiplexed [7, p. 5], then:

```
1 inline void enter(wand_t *wand) {  
2     lock(&wand->clue);  
3 }  
4  
5 inline void leave(wand_t *wand) {  
6     unlock(&wand->clue);  
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```

- wand capability depends on the “type of exclusion” in relation to the required characteristics of the operating system machine level:
 - partial** ■ processor **multiplexing** $\leadsto$ interrupt control
 - mutual** ■ processor **multiplication** $\leadsto$ process lock, see example above
- combination of both is optional, not mandatory, and problem-specific
 - depends on the degree of parallelism (a) allowed for by the application use case and (b) made possible by the ISA level



- given the concept of a binary semaphore, implementation of a **mutex** is straightforward and, absolutely, no black magic:
 - a mutex data structure is composed of two parts:
 - i a binary semaphore used to actually protect the critical section *and*
 - ii a handle that uniquely identifies the process having acquired the mutex⁶

⁶At kernel level, the handle is the pointer to the process descriptor of the process instance. At user level, it is the process identification.



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 - given such a structure, let the following two functions be defined:
 - acquire** – performs the P and registers the current process as owner
 - release** – conditionally unregisters the owner and performs the V
 - in case of a wrong owner, the current process or kernel, resp., panics

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- a corresponding **data type** may be laid out as follows:

```
1 typedef volatile struct mutex {
2     semaphore_t sema;    /* binary semaphore */
3     process_t *link;     /* owning process or 0 */
4 } mutex_t;
```

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Acquire and Release Mutex

```
1 extern void panic(char*) __attribute__((noreturn));
2
3 void acquire(mutex_t *mutex) {
4     P(&mutex->sema);          /* lockout */
5     mutex->link = being(ONESELF); /* register owner */
6 }
7
8 void release(mutex_t *mutex) {
9     if (mutex->link != being(ONESELF)) /* it's not me! */
10        panic("unauthorised release of mutex");
11
12     mutex->link = 0;           /* deregister owner */
13     V(&mutex->sema);          /* unblock */
14 }
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 - presumably, the non-sequential program contains a **software fault** (bug)
 - returning an error code is no option, as one cannot rely on error checking



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 - any other than “raising a non-maskable exception” is a botch job...



Outline

Preface

Fundamentals

Classification

Characteristics

Implementation

Data Structures

Functions

Mutex

Summary



- fundamental concept for cooperation and communication
 - binary and general/counting semaphore, intrinsic primitives P and V
 - correlation to unilateral and multilateral synchronisation
 - differentiation as to mutex (methods v. implementation/entity)

Hint

A binary semaphore is a valid implementation of one of the many “mutex methods”, but not that restrictive as a “mutex entity” need to be.

↳ Hierarchic placement at operating system machine level

- characteristics important in functional and non-functional terms
 - logical or physical waitlist, conformance to the scheduling discipline
 - deregulation of the protection of P against simultaneous processes
 - further shallows such as overtaking of unblocked processes in P :

Hint

Constrict concurrency to no more than what is absolutely necessary.

not least, basic approaches and sketches of an implementation...



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```

1      ...
2      movl    16(%esp), %edi
3      leal    4(%edi), %esi
4      jmp     LBB0_2
5  LBB0_1:
6      movl    _life, %eax
7      movl    %esi, 4(%eax)
8      movl    %esi, (%esp)
9      calll   _unlock
10     calll   _coast
11  LBB0_2:
12     movl    %esi, (%esp)
13     calll   _lock
14     cmpl    $0, (%edi)
15     je      LBB0_1
16     movl    $1, (%edi)
17     movl    %esi, (%esp)
18     calll   _unlock
19     ...

```

■ let the sequence of instructions within P be as follows:

- 2 ■ point at semaphore
- 3 ■ point at lock structure
- address is blocked-on mark
- 12–13 ■ apply for P protection
- 14–15 ■ check binary semaphore S_b
- 16–18 ■ unoccupied, take S_b
- quit P protection, done
- 5 ■ occupied, S_b already taken
- 6 ■ point at process structure
- 7 ■ define blocked-on mark
- 8–9 ■ deregulate P protection
- 10 ■ fall asleep, dream about V



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 - 10 ■ fall asleep, dream about V
 - locking overhead when unoccupied
 - net worth of about 5 instructions ☹
- ↪ non-blocking synchronisation ☺

⁷Take a sledgehammer to crack a nut...



Commonalities and differences as to their possible **internal states**.

- general semaphore S_g :
 - positive ■ $N > 0$ processes will complete $P(S_g)$ without blocking
 - zero ■ $P(S_g)$ will block the running process on the waitlist of S_g
 - negative ■ $P(S_g)$ will block the running process on the waitlist of S_g
 - $|N|$ processes are blocked on the waitlist of S_g
- binary semaphore S_b :
 - not taken ■ exactly one process will complete $P(S_b)$ without blocking
 - the very process becomes **logical owner** of S_b
 - taken ■ $P(S_b)$ will block the running process on the waitlist of S_b
 - $V(S_b)$ should be performed only by the logical owner of S_b
- mutex entity M : let A be *acquire* and let R be *release*
 - not owned ■ exactly one process will complete $A(M)$ without blocking
 - the very process becomes **physical owner** of M
 - owned ■ $A(M)$ will block the running process on the waitlist of M
 - $R(M)$ can succeed only for the physical owner of M



- principle pattern of a scheduler function to block a process
 - called by *coast* (cf. p. 89) and other functions to pause computation

```
1 void block() {
2     process_t *next, *self = being(ONESELF);
3
4     while (!(next = elect(hoard(READY))))
5         relax();                /* no ready to run... */
6
7     if (next != self) {          /* must relinquish */
8         self->state = BLOCKED;  /* vacate processor */
9         seize(next);            /* resume elected */
10    }
11    self->state = RUNNING;        /* occupy processor */
12 }
```

- 4 ■ choose next process to be dispatched to the processor
- 5 ■ ready list is empty, so the running process fades to the idle process
- 7 ■ as the case may be, the running process may be allowed to continue:
 - i the idle/running process found itself ready-to-run on the ready list or
 - ii the running process, sent to sleep due to P , was roused due to V (p. 89)

